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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


OCT 10 2005

Application Serial No. .... 10/626,762  
Filing Date ..... July 24, 2003  
Inventor ..... Walker, et al.  
Assignee ..... IBM Corporation  
Group Art Unit ..... 2825  
Examiner ..... Leigh M. Garbowski  
Attorneys Docket No. .... YOR920030303US1  
Title: SYSTEM AND METHOD FOR DERIVATIVE-FREE OPTIMIZATION OF ELECTRICAL  
CIRCUITS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF FACSIMILE TRANSMISSION**

I hereby certify that this Response to Restriction Requirement (1 page) is being facsimile transmitted under Rule 37 CFR 1.61(d) to the U.S. Patent and Trademark Office to (571) 273-8300 on October 10, 2005.

 10/10/05  
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RESPONSE TO RESTRICTION REQUIREMENT

To: Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: Satheesh K. Karra  
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REMARKS

In response to the October 3, 2005 Restriction Requirement, Applicants elect Group I, Claims 1-13, and 32 - 38 for prosecution on the merits. Applicants reserve the right to file one or more divisional applications directed to the subject matter of the non-elected claims.

The undersigned is available for telephone consultation at any time.

Respectfully submitted,

Dated: October 10, 2005

By:

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